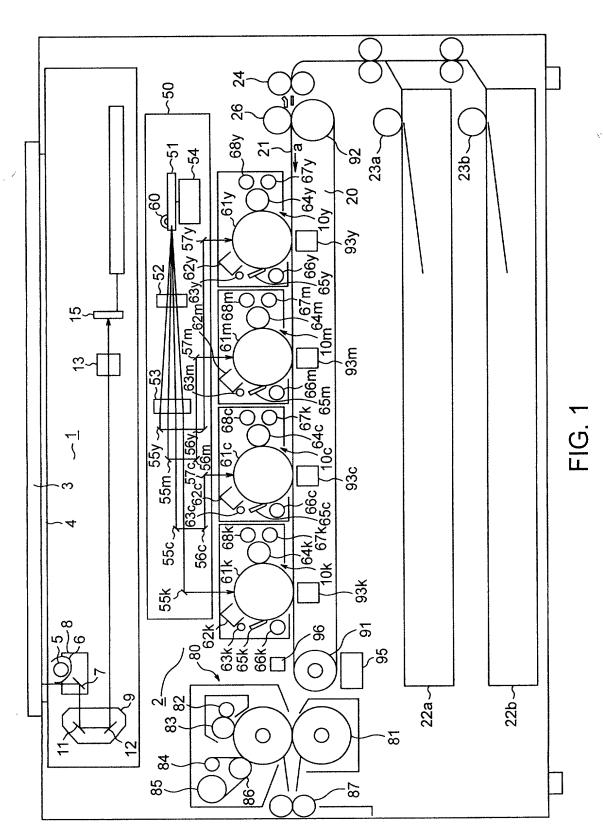
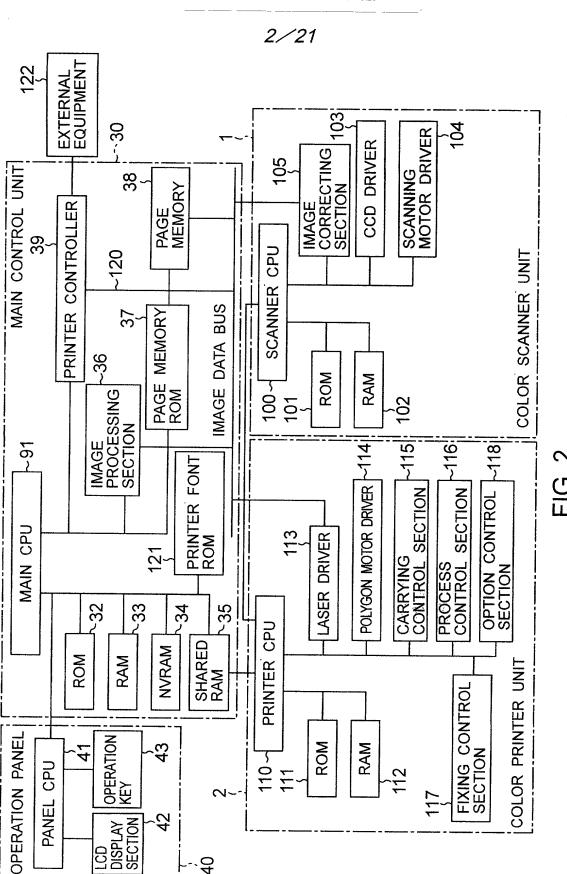
Title: IMAGE PROCESSING APPARATUS AND IMAGE PROCESSING SYSTEM Inventor(s): Eiichi SAKAUE et al.

DOCKET NO.: 016887/1029



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DISPLAY

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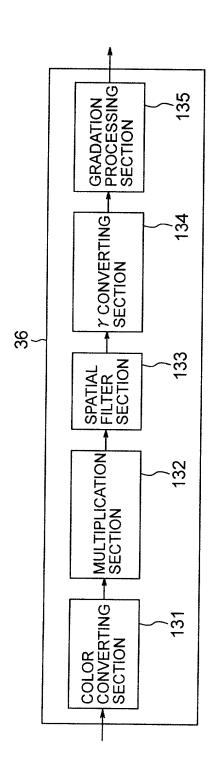
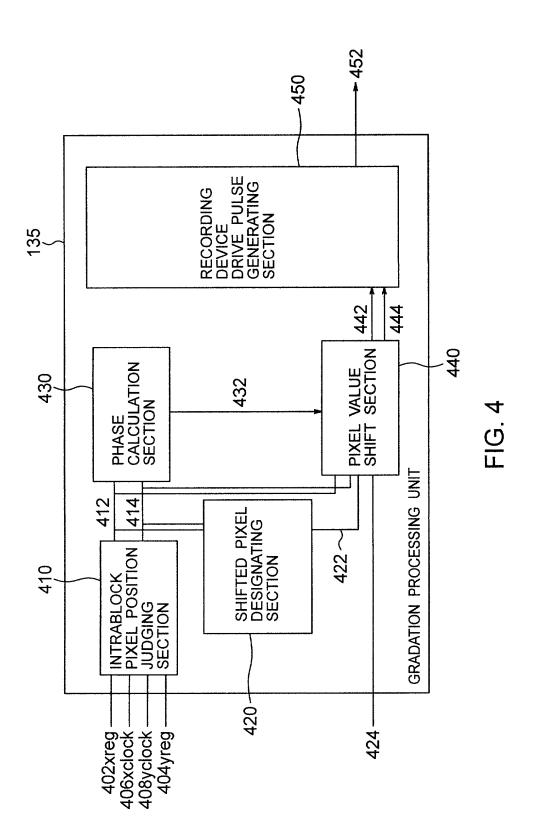
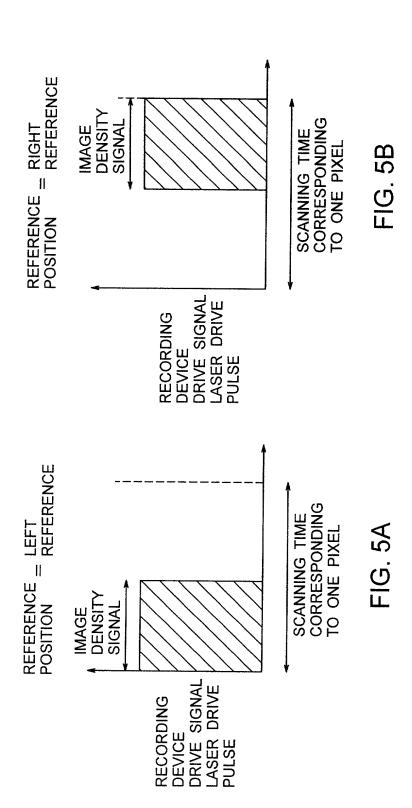


FIG. (

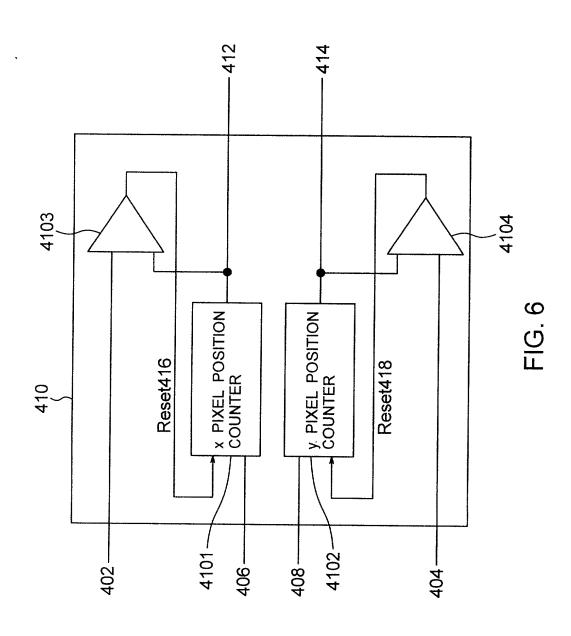
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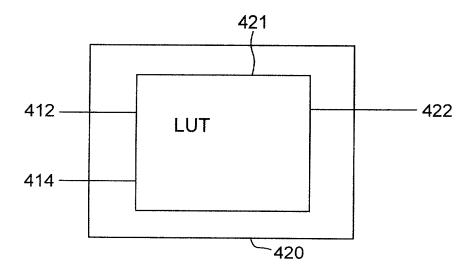


FIG. 7

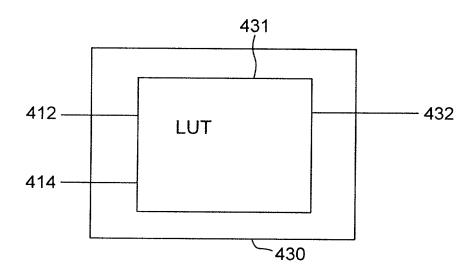


FIG. 8

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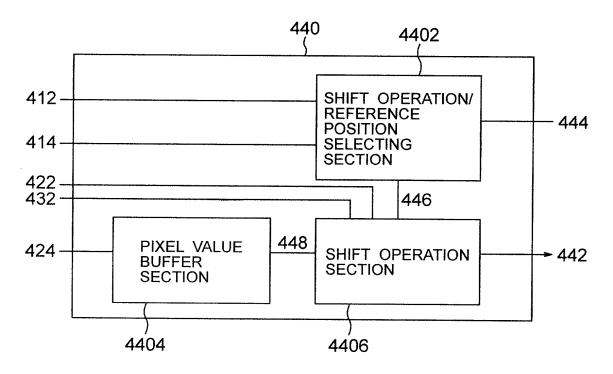


FIG. 9

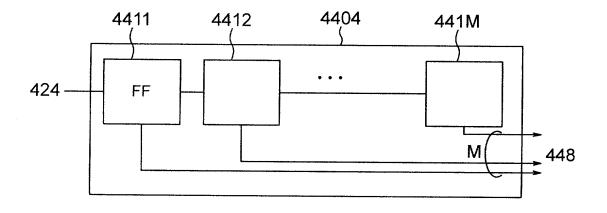
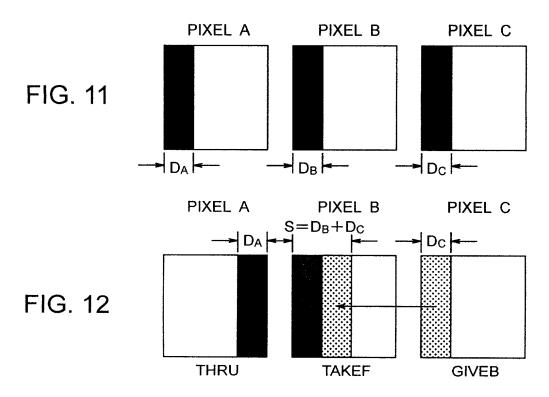
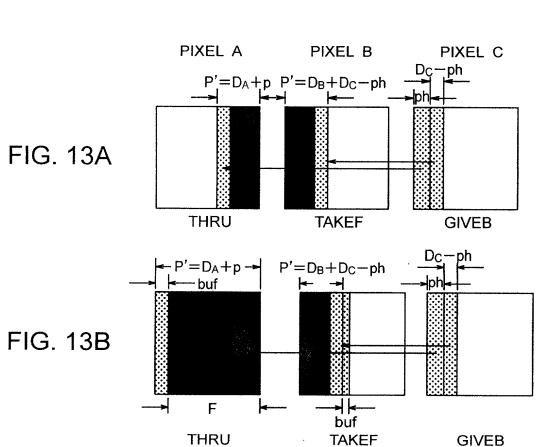


FIG. 10

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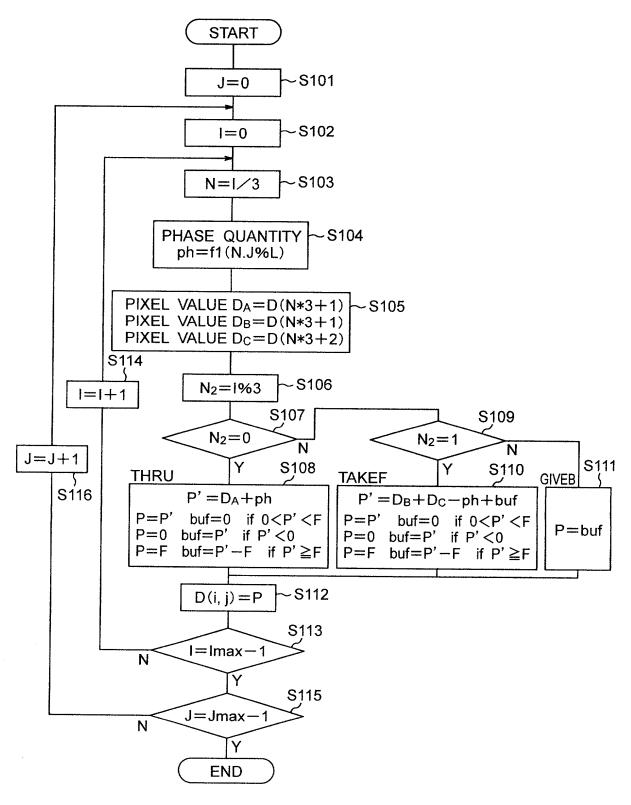
OPERATION NAME	PIXEL VALUE OF PIXEL TO BE PROCESSED	WRITE SHIFT BUFFER VALUE	CONDITION
THRU	0	Д.	Ph≥0 & Pf1+Pf2+P≥xff×2
	$Pf1+Pf2+P-xff\times2$	xff x 2 Pf1 +- Pf2	Ph≧0 & Pf1+Pf2+P <xff×2< td=""></xff×2<>
	P+Ph	Ph	Ph<0 & Pf1+Pf2≧Ph
	P+P1+P2	-Ph-(Pf1+Pf2)	Ph<0 & Pf1+Pf2 <ph< td=""></ph<>
TAKEF	xff	P+Pf+Pbuf-xff	P+Pf+Pbuf≧xff
	P+Pf+Pbuf	0	P+Pf+Pbuf <xff< td=""></xff<>
GIVEB	Pbuf	0	

OPERATION NAME	PIXEL VALUE OF PIXEL TO BE PROCESSED	WRITE SHIFT BUFFER VALUE	CONDITION
THRU	Ь	0	Ph≥0 & Pf1+Pf2+P≥xff×2
TAKEF	xff	P+Pf-xff	P+Pf≧xff
	P+Pf	0	P+Pf <xff< td=""></xff<>
GIVEB	Pbuf	0	

FIG 17

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FIG. 15

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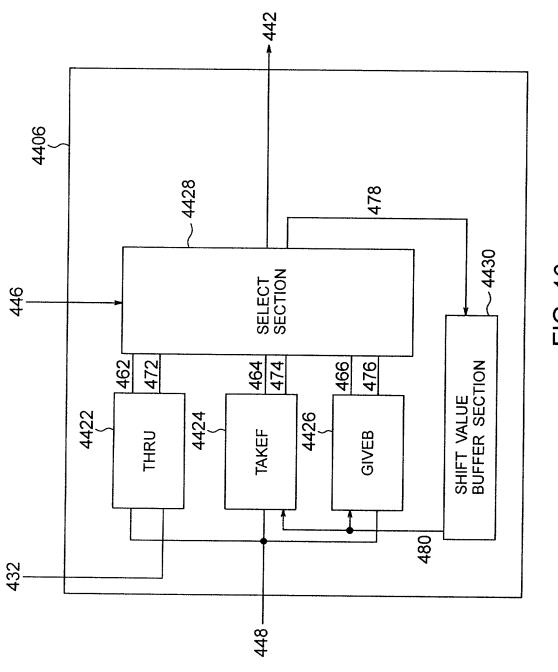


FIG. 16

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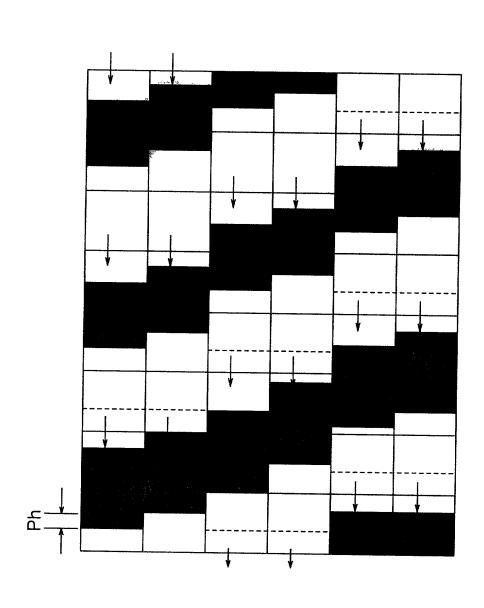


FIG. 18

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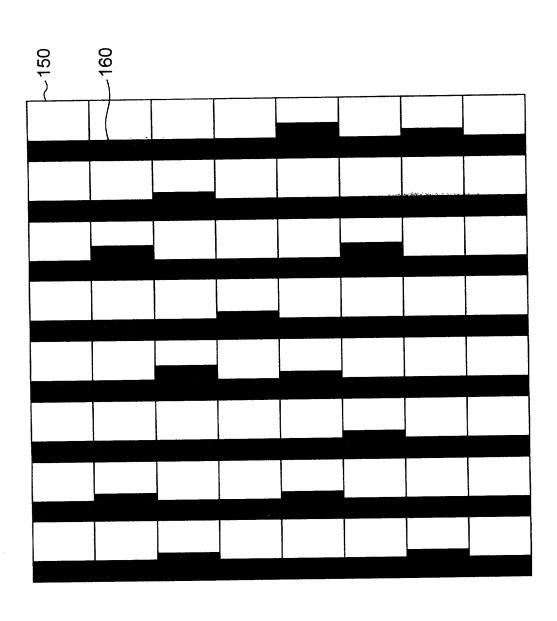


FIG. 19

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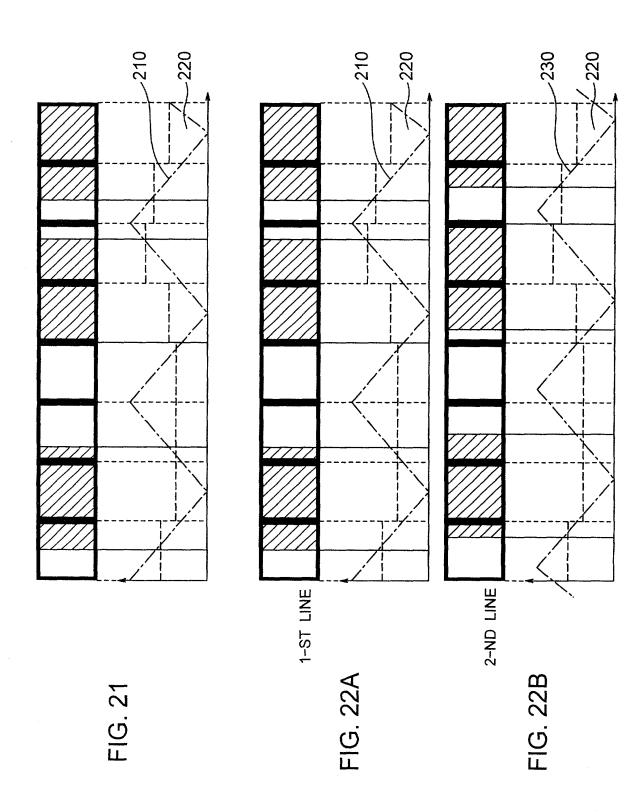
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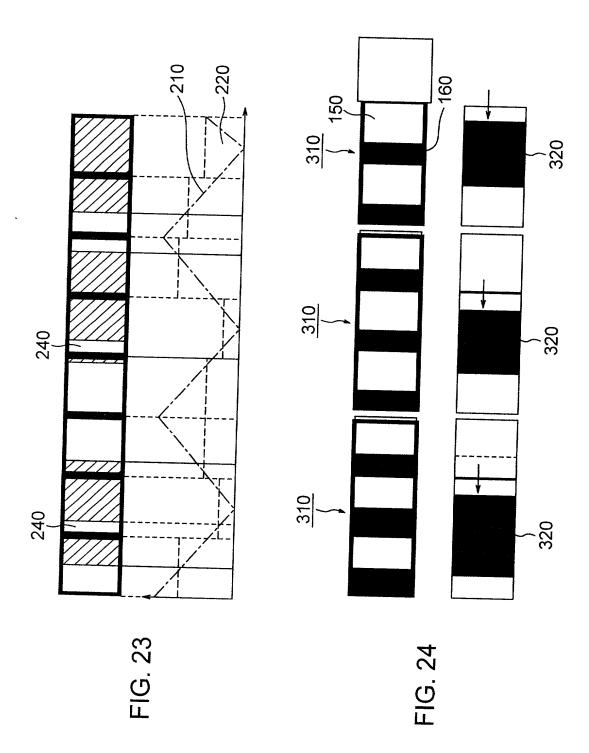
FIG. 20

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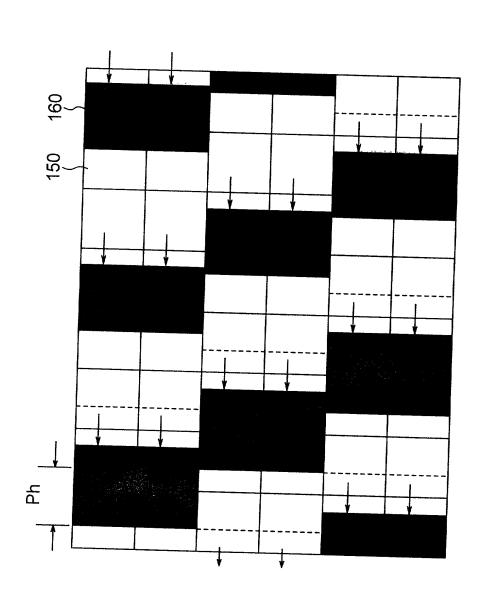
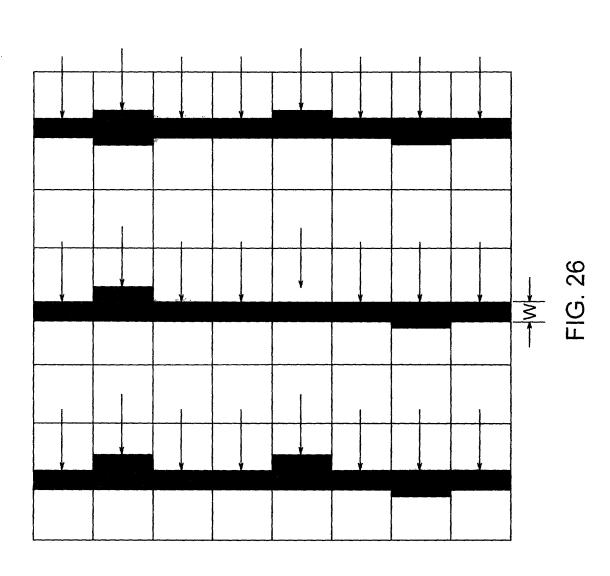


FIG. 25

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Inventor(s): Eiichi SAKAUE et al.
DOCKET NO.: 016887/1029

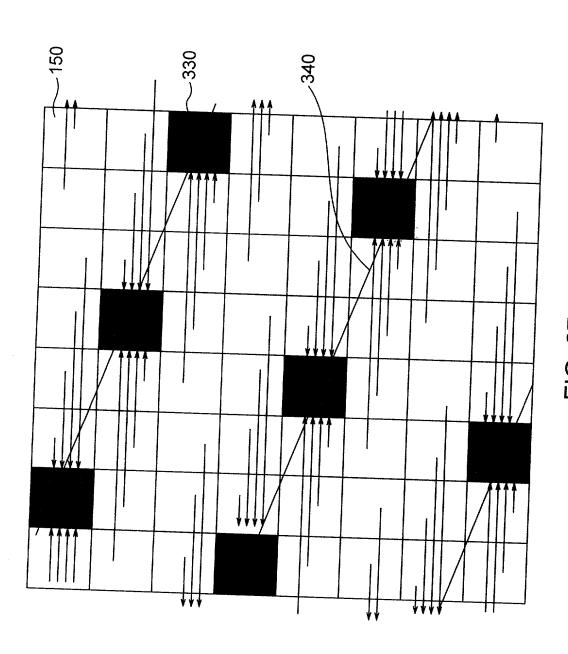


FIG. 27

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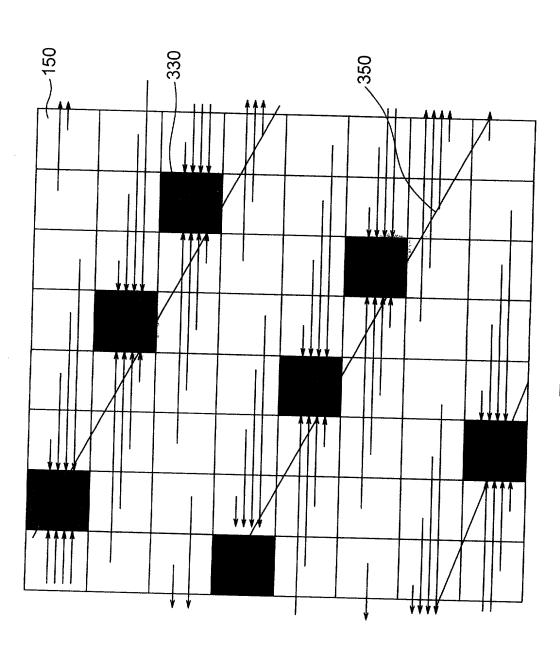


FIG. 28